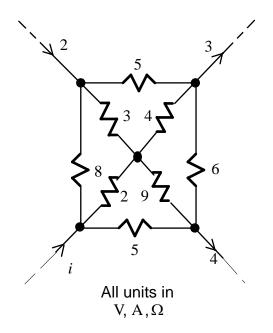
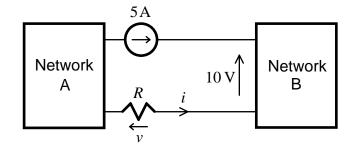
TUTORIAL PROBLEMS

E.1 KCL, KVL, Power and Energy

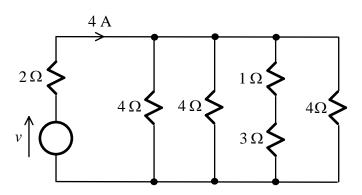
Q.1 Determine the current *i* in the following circuit.



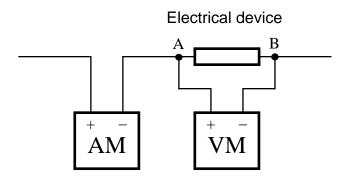
Q.2 Determine the current *i* and the voltage *v* in the following circuit for $R = 2\Omega$ and $R = 50 \text{ K}\Omega$.



Q.3 Determine the source voltage v and the voltage across the 3Ω resistor in the following circuit.



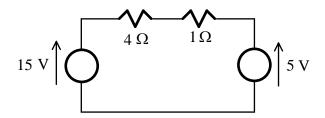
Q.4 The Ammeter AM and voltmeter VM, connected as shown below, measure current and voltage, respectively. The ammeter will give a positive reading if the current flowing into its "+" terminal is positive.



Determine if the electrical device is consuming or supplying power when

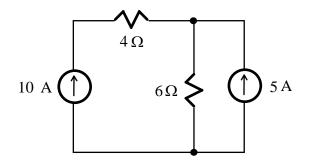
- (a) both meters give positive readings;
- (b) both meters give negative readings; and
- (c) one meter gives a positive reading while the other gives a negative reading.
- (d) one meter or both meters give zero readings.

Q.5 Determine the current in the following circuit.



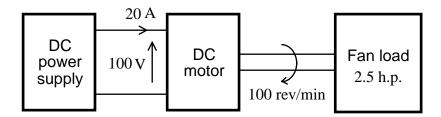
How much power is each component consuming or supplying?

Q.6 Determine the voltages and currents in the following circuit.



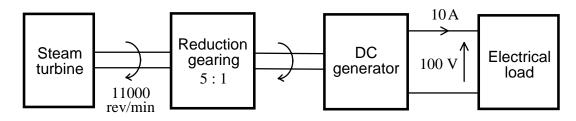
Is the 10A current source consuming or supplying power?

Q.7 In the following system, determine the efficiency of the motor and the torque on the fan shaft?



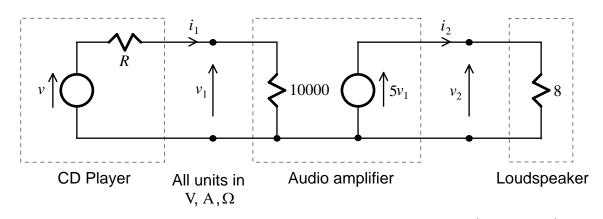
How much energy is lost in the motor per minute? (Note that 1h.p. = 746W.)

Q.8 In the following system, the efficiency of the generator is 0.9.



Determine the torque in its shaft.

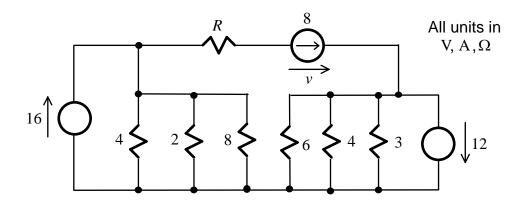
Q.9 The following figure shows the equivalent circuit of an audio amplifier when connected to an input audio source (CD player) and a load (loudspeaker).



Calculate the voltage gain $|v_2/v_1|$, current gain $|i_2/i_1|$ and power gain $|(v_2i_2)/(v_1i_1)|$ of the amplifier in dB. What is the relationship between these gains? Show that these gains will be equal to one another when the load resistance is equal to input resistance of the amplifier. Do you expect this to be the case in practice?

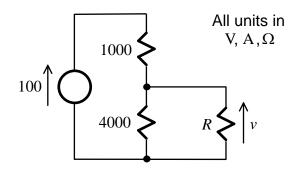
E.2 KCL, KVL and Grounding

Q.1 Determine the current in each branch of the following circuit by inspection.



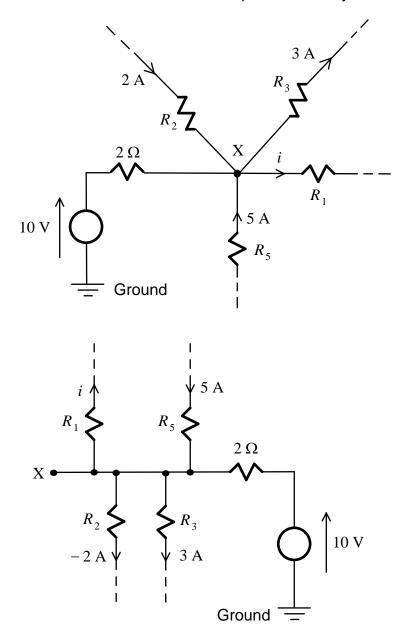
If the voltage v is -16V, determine R.

Q.2 The following circuit shows a common voltage divider for obtaining a certain voltage v across a load resistor R.



In finding v, a novice (=1st year student) may forget (okay, if it's only for this time) to include the loading effects of R. To understand these effects, determine v and the current in R when

- (a) $R = \infty$ (open circuit or no load situation);
- (b) $R = 8000 \Omega$;
- (c) $R = 200\Omega$; and
- (d) R = 0 (short circuit situation).

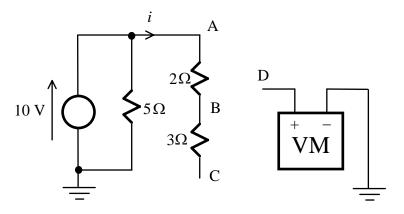


Q.3 Are the two circuits shown below equivalent? Why?

As is done in the above circuits, voltages in electrical systems are often measured with respect to a reference or ground potential. In practice, the ground will be connected to a large number of components and will run through the entire system using thick conductors. It is sometimes connected to the chassis of the system or the earth pin of the 3-pin power plug.

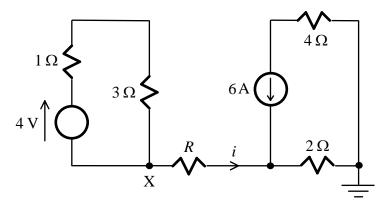
Assuming that there are other components (not shown) connected to ground, determine the current flowing through the voltage source and the potential of node X with respect to ground when i = 2A and i = -3A. Why is the ground node seems to be taking/giving current all the time and still Kirchhoff's current law is not violated?

Q.4 In the following circuit, the voltmeter VM is used to measure the voltages of various points with respect to ground.

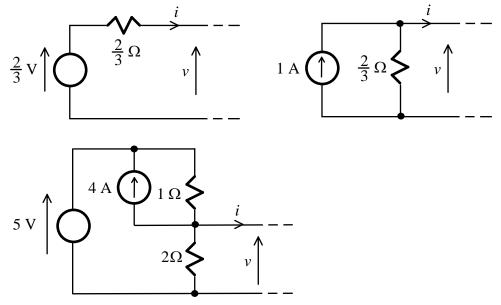


Assuming that VM does not take any current, determine the current i and the voltmeter reading when

- (a) Point C is grounded and Point B is connected to Point D;
- (b) there is no connection for Point C and Point B is connected to Point D; and
- (c) Point B is grounded and Point C is connected to Point D.
- Q.5 Determine the current *i* and the potential of node X with respect to ground in the following circuit.



How will these be changed if the circuit is not grounded or if the circuit is grounded at Point X only?

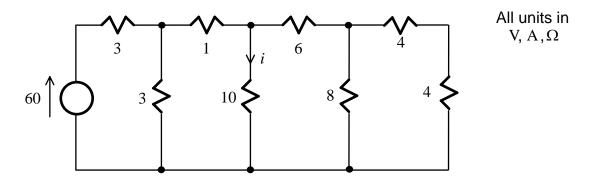


Q.6 Determine the relationships between v and i for the following three circuits.

What can you say about these three circuits?

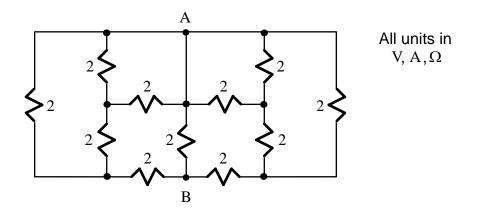
E.3 DC Circuit Analysis I

Q.1 Find the source current in the following ladder network by reducing the series and parallel combinations to a single resistor.



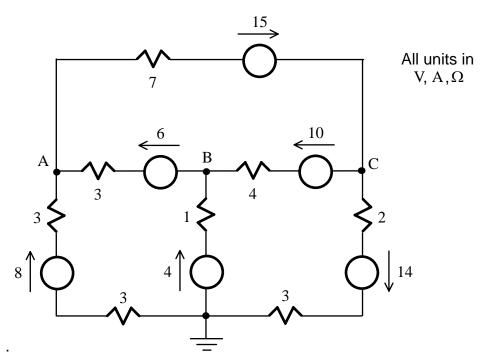
Determine the current i by using current division.

Q.2 Determine the equivalent resistance that would be measured between Points A and B of the following circuit.



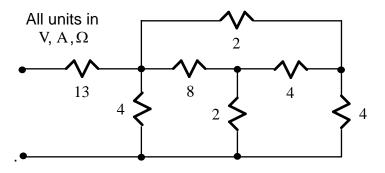
What will the result be if the outer two resistors are short-circuited?

Q.3 Formulate the equations for obtaining the loop currents of the following circuit by using mesh analysis. Determine the voltages of nodes A, B and C with respect to ground in terms of these currents.

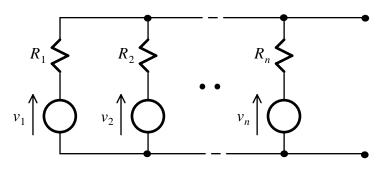


Using nodal analysis, derive the equations for obtaining the voltages of the three nodes.

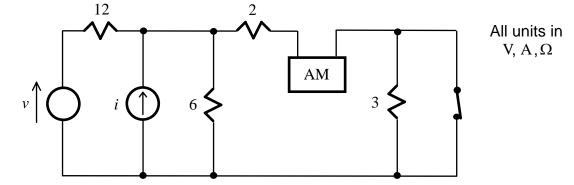
Q.4 Using nodal analysis, calculate the equivalent resistance of the following 2terminal network.



Q.5 Determine Norton's and Thevenin's equivalent circuits for the following 2terminal network.

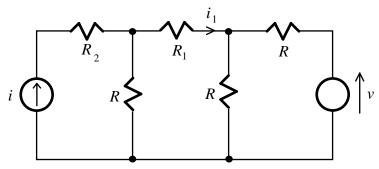


Q.6 In the following circuit, the ammeter AM reads 1A when the switch is closed.



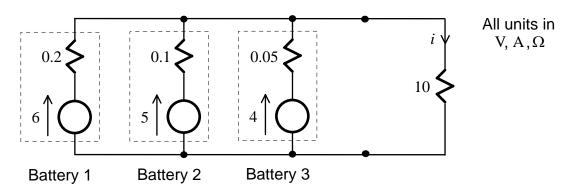
Using Norton's equivalent circuit, determine the reading when the switch is open.

Q.7 Using Thevenin's equivalent circuit, determine the current i_1 In the following circuit.



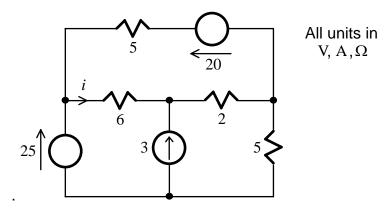
E.4 DC Circuit Analysis II

Q.1 The following circuit shows three batteries connected in parallel and supplying power to a 10Ω load.

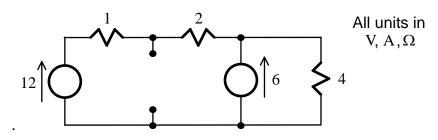


Using superposition, determine the component of the load current i due to each battery. Hence, calculate the load current.

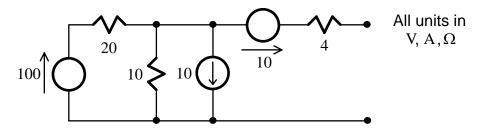
Q.2 Determine the current *i* in the following circuit by using superposition.



Q.3 Determine the maximum power that can be obtained from the two terminals of the following circuit.

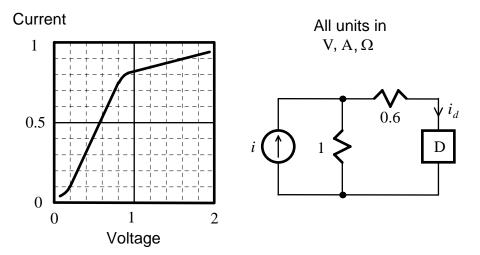


Q.4 Determine the resistor that will draw a current of 2A when connected across the two terminals of the following network.



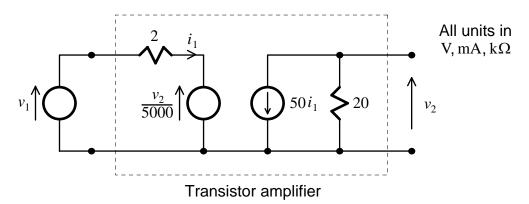
What resistor will absorb the maximum power? Calculate this power.

Q.5 The following figure shows the voltage-current curve of an electrical device D and a circuit in which the device is employed.



Determine the device current i_d if i = 1.6 A. At what value of *i* will the power dissipated in D be 0.6W?

Q.6 The following figure shows the equivalent circuit of a transistor amplifier connected to amplify the magnitude of the voltage v_1 from an input voltage source. The main activities of the transistor are represented by the two dependent sources, while the substrate and other contact resistances are represented by the two resistors.



Determine the output voltage v_2 of the amplifier in terms of v_1 . What is the gain of the amplifier in terms of voltage magnitudes? Give your answer in dB. Determine the Thevenin's equivalent circuit of the system as seen from the output terminals.

E.5 AC Circuit Analysis I

Q.1 Write down the peak and rms values, frequency, phase, complex and phasor representations of the following ac quantities.

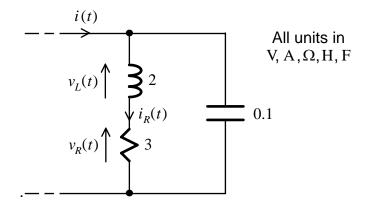
(a)
$$5\sqrt{2} \sin(\omega t)$$

(b) $5\sqrt{2} \cos(\omega t)$
(c) $10\sqrt{2} \sin(20t+30^{0})$
(d) $120\sqrt{2} \cos(314t-45^{0})$
(e) $-50 \sin\left(4t-\frac{\pi}{3}\right)$
(f) $0.25 \cos(2t+100^{0})$

Q.2 Taking the frequency to be 50 Hz, write down the sinusoidal voltages and currents corresponding to the following phasors.

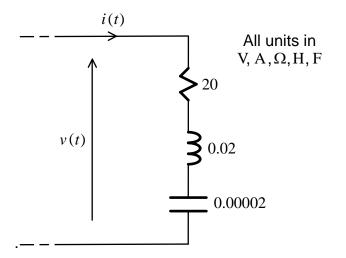
(a)
$$\frac{100}{\sqrt{2}}e^{j30^{\circ}}$$
 V
(b) $115e^{j\pi/3}$ V
(c) $-0.12e^{-j\pi/4}$ A
(d) $-0.69/60^{\circ}$ A

Q.3 In the following ac circuit, $v_R(t) = 12\sqrt{2}\cos(2t)$ V.



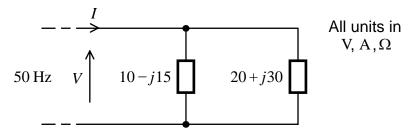
Determine $i_R(t)$, $v_L(t)$ and i(t) by using phasor analysis.

Q.4 In the following series RLC circuit, $v(t) = 50\sqrt{2}\cos(1250t + 30^0)$ V. Determine the phasors of the voltages across the three components and that of the current *i*(*t*).



Draw a phasor diagram showing all these phasors in the complex plane.

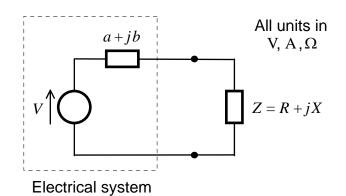
Q.5 The following figure shows an ac circuit with two parallel branches. Each branch consists of two series components: *RL* (resistor and inductor) or *RC* (resistor and capacitor) or *LC* (inductor and capacitor). From the impedances given, find the components and their values in the circuit.



Calculate the admittance (reciprocal of impedance) of the entire circuit and the phase angle between I and V. What is the power factor of the circuit?

E.6 AC Circuit Analysis II

- Q.1 A 4Ω resistor is connected in series with a coil across a 20V 50 Hz supply. The voltage across the resistor is 9V and that across the coil is 14V. If the coil can be represented as an ideal inductor *L* in series with a resistor *R* (accounting for the winding resistance and other losses), calculate the values of *L* and *R*, the power absorbed by the coil, and the power factor of the whole circuit.
- Q.2 A load connected across a 2000 V 50 Hz line draws 10 kW at a lagging power factor of 0.5. Determine the current taken by the load. To improve the power factor, a capacitor *C* is now connected in parallel with the load. Determine the value of *C* so that the overall power factor is 0.91agging, unity and 0.81eading.
- Q.3 The following diagram shows the Thevenin's equivalent circuit of an ac system when connected to a load with impedance Z = R + jX.

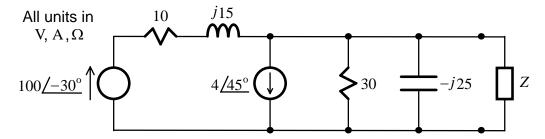


Show that R and X should be given by

 $Z = R + jX = (a + jb)^* = a - jb$

in order for the load to absorb the maximum power. Determine this maximum power.

Q.4 Using Norton's equivalent circuit, determine the maximum power that can be absorbed by the load with impedance *Z* in the following ac system.

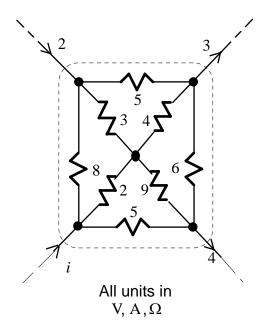


- Q.5 A certain tune circuit consists of a $100 \ \mu$ H coil with a resistance of 10Ω (representing losses due to thermal resistance and other effects) connected in series with a 100 pF capacitor. Determine the resonant frequency, *Q* factor, bandwidth and 3dB cutoff frequencies of the circuit.
- Q.6 A variable tuning capacitor in a radio receiver has a maximum capacitance of 500 pF and a minimum capacitance of 20 pF. What inductance is required so that the lowest frequency to which the circuit can be tuned is 666 kHz? What is the highest frequency to which this circuit can be tuned?

TUTORIAL SOLUTIONS

F.1 KCL, KVL, Power and Energy

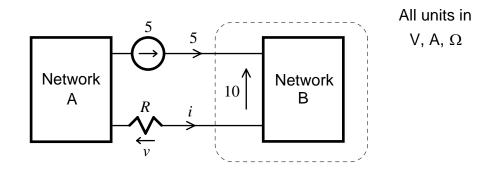
Q.1



Applying KCL to the dotted surface:

$$i + 2 = 3 + 4 \implies i = 5$$

Q.2



Applying KCL to the dotted surface:

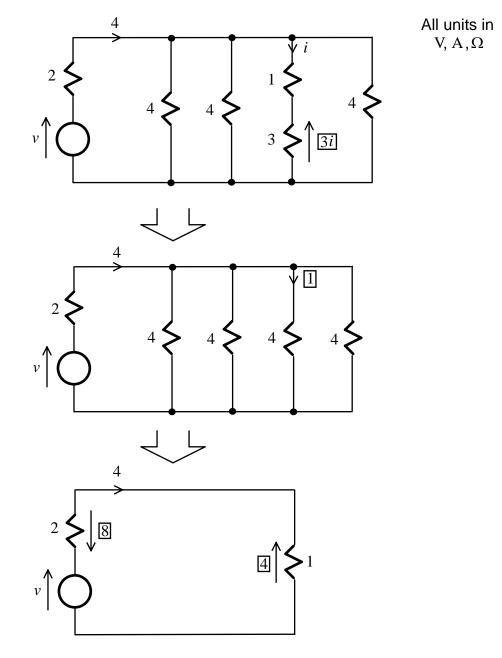
 $5+i=0 \implies i=-5A$

regardless of the value of *R*. For $R = 2\Omega$,

$$v = iR = -5 \times 2 = -10$$
 V

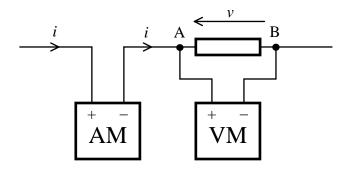
For $R = 50 \text{ K}\Omega$,

$$v = iR = -5 \times 50000 = -250 \,\mathrm{KV}$$



v = 8 + 4 = 12 V; i = 1; voltage across 3Ω resistor = 3i = 3 V

(a) Both meters give positive readings



Since the arrows for v and i are in opposite directions

Power consumed = vi

Also, AM will give a positive reading if i is positive, while VM will give a positive readings if v is positive.

Since both *i* and *v* are positive in this case, vi is positive and power is consumed.

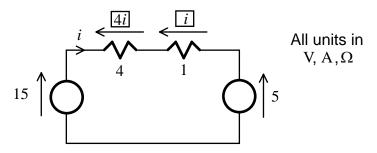
(b) Both meters give negative readings

Both i and v are negative, vi is positive and power is consumed.

- (c) One meter gives a positive reading and the other gives a negative reading
 i and *v* have opposite signs, *vi* is negative and power is supplied by the device.
- (d) One meter or both meter give zero readings

Power neither is consumed nor supplied by the device.

Current in circuit



Applying KVL:

 $15 = 4i + i + 5 \implies i = 2$

Power consumed/supplied

If the voltage and current arrows are in opposite directions,

Power consumed = (voltage)(current)

Thus:

Power consumed by 4Ω resistor = i(4i) = 16 W

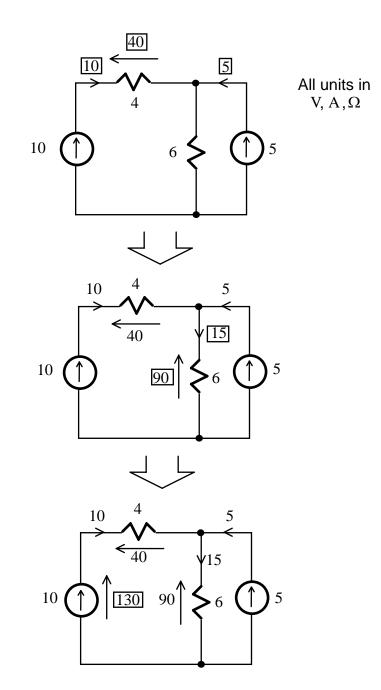
Power consumed by 1Ω resistor = i(i) = 4 W

Power consumed by 5 V source = i(5) = 10 W

Power consumed by 15 V source = -i(15) = -30 W

or

Power supplied by 15V source= 30W



The 10A current source is supplying a power of (130)(10) = 1300 W.

Efficiency

Electrical power supplied =(100)(20) = 2000 W

Mechanical power delivered =
$$(2.5 \text{ h. p.})(746 \text{ W/h. p.}) = 1865 \text{ W}$$

Efficiency $=\frac{1865}{2000}=93.25\%$

Torque

Motor speed =
$$\left(100 \,\text{rev}/\text{min}\right) \left(\frac{2\pi}{60} \frac{\text{rad}/\text{rev}}{\text{s}/\text{min}}\right) = \frac{20\pi}{6} \,\text{rad/s}$$

Torque =
$$\frac{\text{Mechanical power delivered}}{\text{motor speed}} = \frac{1865}{20\pi/6} = 178.1 \text{ Nm}$$

Energy lost

Power lost = 2000 - 1865 = 135 W

Energy lost per min = (135)(60) = 8100 J

Q.8

Generator output power = (100)(10) = 1000 WGenerator input power = $\frac{1000}{0.9}$ = 1111.1 W Generator shaft speed = $\left(\frac{11000}{5} \text{ rev}/\text{min}\right) \left(\frac{2\pi}{60} \frac{\text{rad}/\text{rev}}{\text{s}/\text{min}}\right) = 230.4 \text{ rad/s}$ Torque = $\frac{1111.1}{230.4}$ = 4.82 Nm

Voltage, current and power gains for system

Voltage gain =
$$g_v = \left| \frac{v_2}{v_1} \right| = \left| \frac{5v_1}{v_1} \right|$$

= 5 = 20log(5) dB = 14 dB
Current gain = $g_i = \left| \frac{i_2}{i_1} \right| = \left| \frac{v_2/8}{v_1/10000} \right|$
= $\left| \frac{5v_1/8}{v_1/10000} \right| = 6250 = 20\log(6250) dB = 76 dB$
Power gain = $g_p = \left| \frac{v_2 i_2}{v_1 i_1} \right| = g_v g_i$
= (5)(6250) = 10log[(5)(6250)] dB
= $\frac{1}{2} [20\log(5) + 20\log(6250)] dB = \frac{14 + 76}{2} dB = 45 dB$

Relationship between these gains

$$g_p = g_v g_i$$

 $\left(g_p \, \mathrm{dB}\right) = \frac{\left(g_v \, \mathrm{dB}\right) + \left(g_i \, \mathrm{dB}\right)}{2}$

 $g_p = g_v = g_i$ if load resistance equals amplifier's input resistance

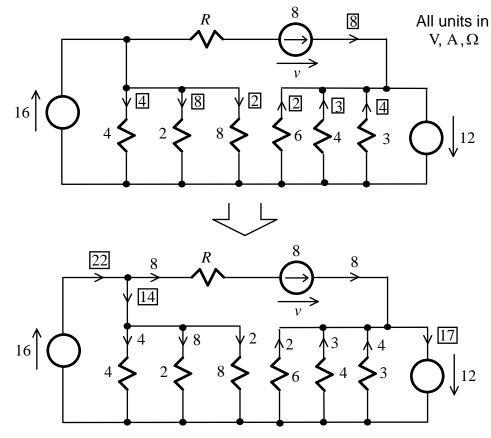
Audio amplifier

Most loudspeakers have resistances in the order of a few Ω . However, in order not to load the CD player or other audio input equipment, the input resistance of the amplifier will have to be large and is usually greater than many $k\Omega$.

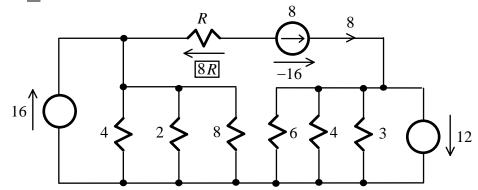
F.2 KCL, KVL and Grounding

Q.1

Currents



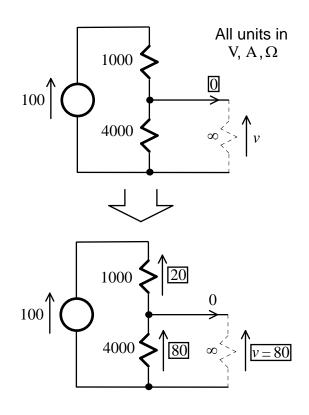
Value for <u>R</u>



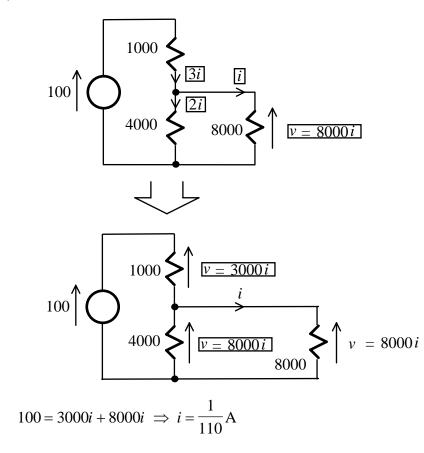
Applying KVL to the loop with the sources and *R*:

$$16 - 8R - 16 + 12 = 0 \implies R = 1.5$$

(a) $R = \infty$ (open circuit or no load situation)

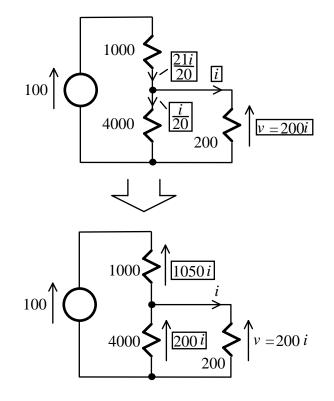


(b) $R = 8000 \Omega$



$$v = 8000i = 72.73V$$

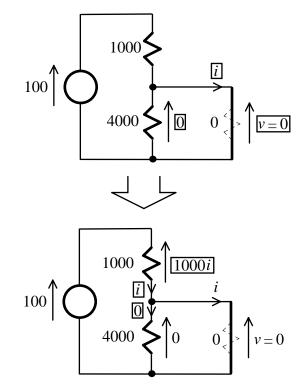
(c)
$$R = 200\Omega$$



$$100 = 1050i + 200i \implies 100 = 1250i \implies i = \frac{2}{25}A$$

v = 200i = 16 V

(d) R = 0 (short circuit)

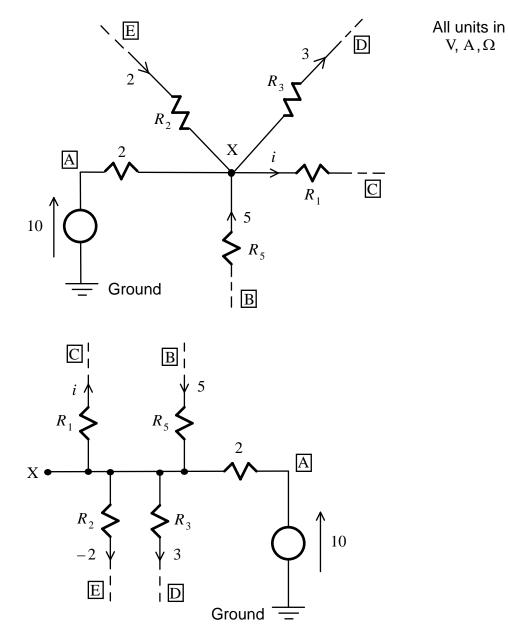


 $100 = 1000i \implies i = 0.1 \text{A}$

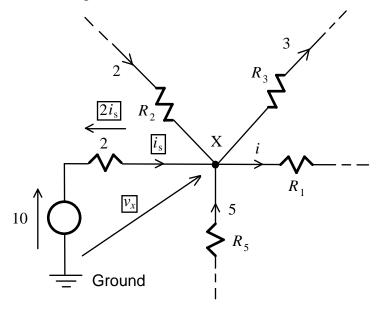
It may be slightly faster to derive two general formulas for v and i and then substitute the values for R.

Equivalence of circuits

The two circuits are equivalent because the connections (topology), elements and currents between the various nodes are identical:



Current and voltage



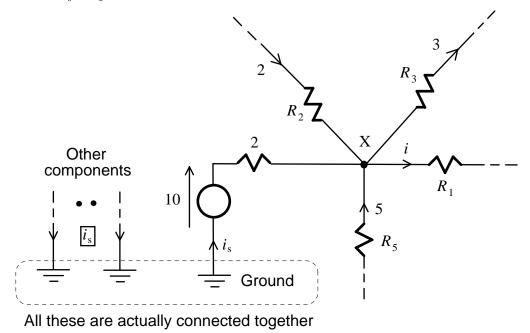
Applying KCL to node ${\rm X}$ and then KVL:

$$i_s = i + 3 - 2 - 5 = i - 4$$

 $v_x = 10 - 2i_s = 10 - 2(i - 4) = 18 - 2i$
When $i = 2$, $i_s = -2A$ and $v_x = 14$ V
When $i = -3$, $i_s = -7A$ and $v_x = 24$ V

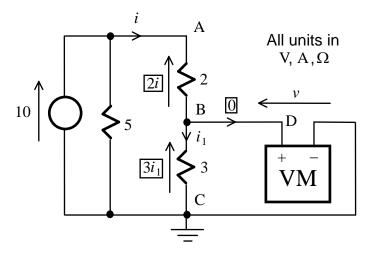
KCL for ground node

Since there may be other components connected to ground, the application of KCL must include all the other connections not shown in the original diagram. The implication is that all these other components must be delivering a combined current of i_s to ground:



Q.4

(a) Point C grounded and Point B connected to Point D



Applying KCL to node B:

$$i_1 = i - 0 = i$$

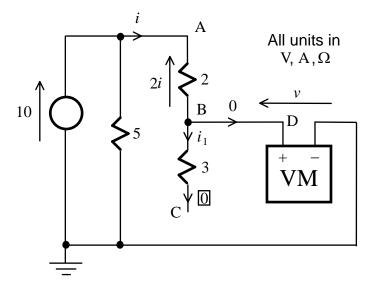
Applying KVL to loop with voltage source, A, B and C:

 $10 = 2i + 3i_1 = 5i \implies i = 2 \mathrm{A}$

Applying KVL to loop with B, C and VM:

 $v = 3i_1 = 3i = 6$ V

(b) No connection for Point C and Point B connected to Point D



Applying KCL to node C and then to node B:

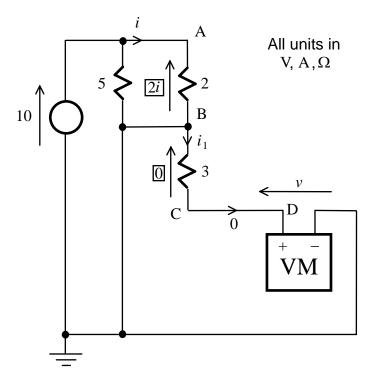
$$i_1 = 0 A$$

$$i = i_1 + 0 = 0 \mathbf{A}$$

Applying KVL to loop with voltage source, A, B and VM:

 $10 = 2i + v \implies v = 10 \text{ V}$

(c) Point B grounded and Point C connected to Point D



Applying KVL to loop with voltage source, A and B:

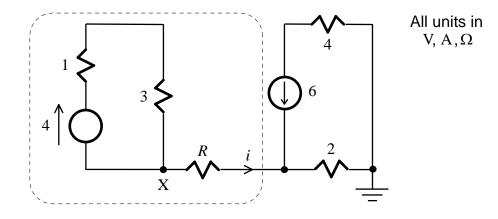
 $10 = 2i \implies i = 5A$

Applying KVL to loop with B, C, D and VM:

$$v + 3i_1 = 0 \implies v = 0$$
 V

Q.5

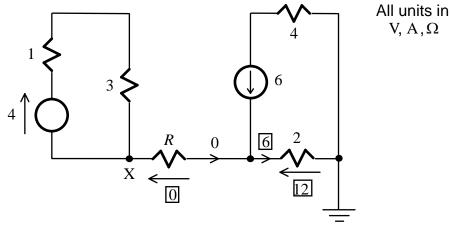
Original circuit



Applying KCL to the dotted surface:

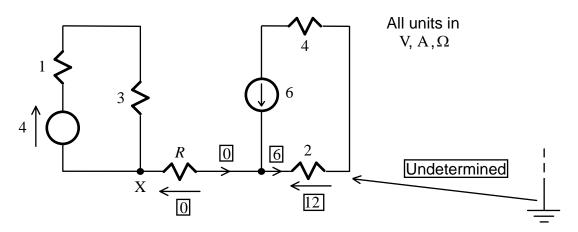
$$i = 0$$

Thus:



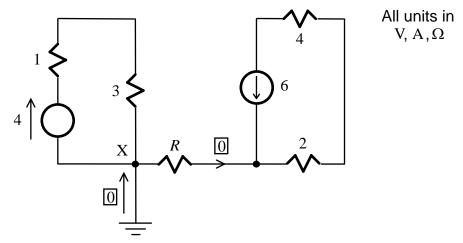
Potential of node X wrt ground = 12 V

When the circuit is not grounded



The potential of node X wrt ground cannot be determined. In practice, its value will depend on factors such as the existence of static charges and other electrical and magnetic effects.

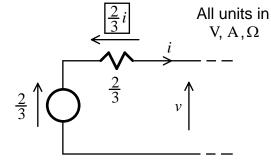
When Point X is grounded



The potential of node X wrt ground is now 0.

Q.6

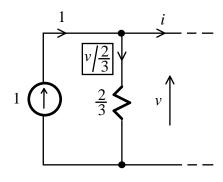
First circuit



Applying KVL:

$$\frac{2}{3} = v + \frac{2i}{3}$$

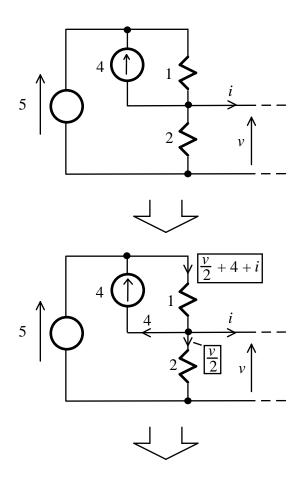
Second circuit

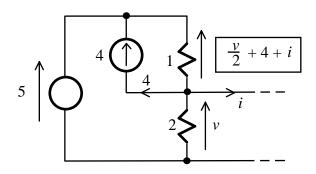


Applying KCL:

$$1 = \frac{v}{2/3} + i \implies \frac{2}{3} = v + \frac{2i}{3}$$

Third circuit



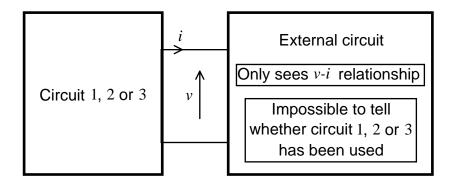


From KVL:

$$5 = \frac{v}{2} + 4 + i + v = \frac{3v}{2} + 4 + i$$
$$1 = \frac{3v}{2} + i \implies \frac{2}{3} = v + \frac{2i}{3}$$

Equivalence

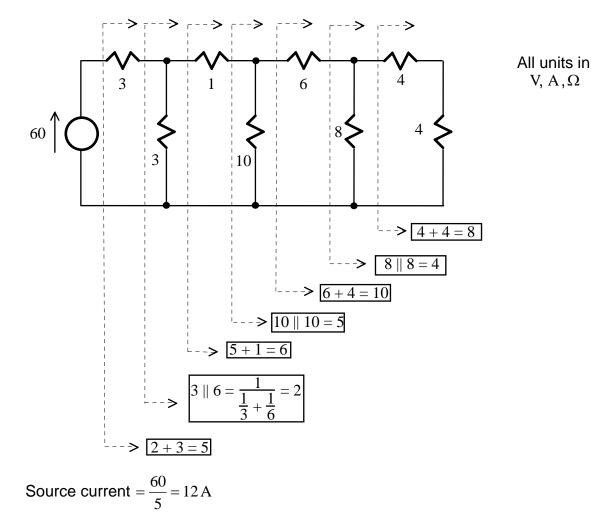
The three circuits are different in circuit topology and the components used. However, they have the same voltage-current relationship and are electrically equivalent from a voltage-current point of view. It is impossible for an external circuit connected to the outputs of these circuits to tell which circuit has actually been used:



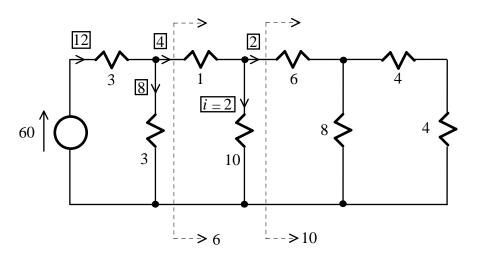
F.3 DC Circuit Analysis I

Q.1

Source current

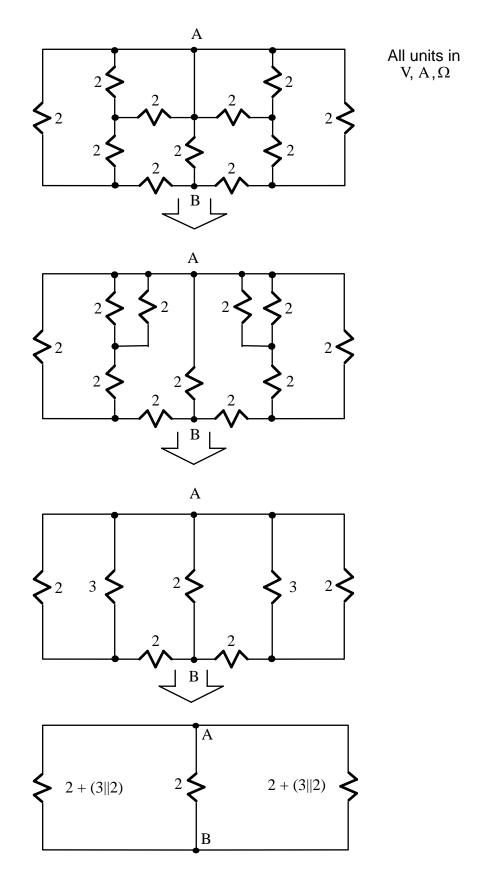


Value for i



All units in V, A,Ω

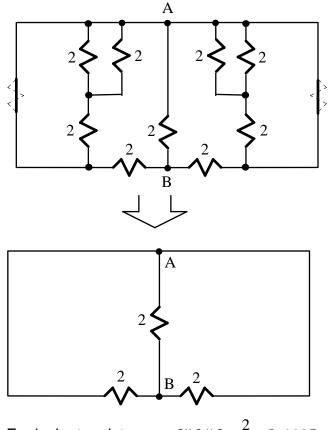
Original circuit



$$2 + (3||2) = 2 + \frac{1}{\frac{1}{3} + \frac{1}{2}} = 2 + \frac{6}{5} = 3.2$$

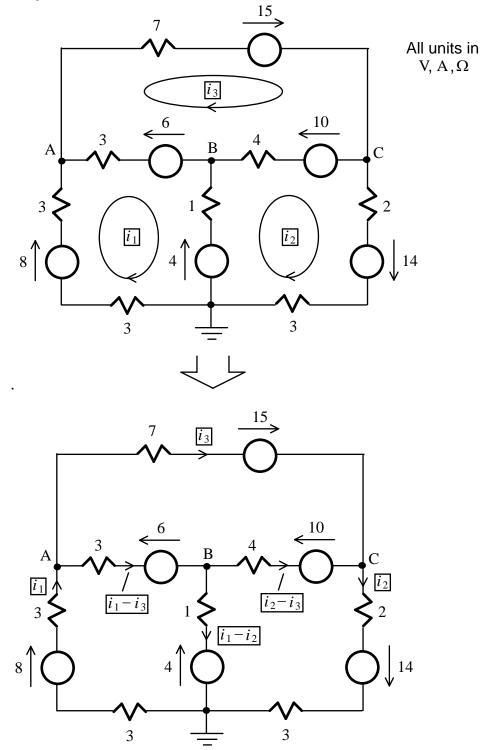
Equivalent resistance = 2 || 3.2 || 3.2 = 2 || 1.6 = $\frac{1}{0.5 + 0.625} = 0.889 \Omega$

When outer two resistors are short-circuited



Equivalent resistance = $2||2||2 = \frac{2}{3} = 0.666 \Omega$

Mesh analysis



Applying KVL for the three loops shown:

$$\begin{split} 8 - 3i_1 - 3(i_1 - i_3) - 6 - (i_1 - i_2) - 4 - 3i_1 &= 0 \\ 14 - 3i_2 + 4 - (i_2 - i_1) - 4(i_2 - i_3) - 10 - 2i_2 &= 0 \\ 15 + 10 - 4(i_3 - i_2) + 6 - 3(i_3 - i_1) - 7i_3 &= 0 \end{split}$$

Simplifying:

$$-2 = 10i_1 - i_2 - 3i_3$$
$$8 = -i_1 + 10i_2 - 4i_3$$
$$31 = -3i_1 - 4i_2 + 14i_3$$

In matrix form:

$$\begin{bmatrix} 10 & -1 & -3 \\ -1 & 10 & -4 \\ -3 & -4 & 14 \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \\ i_3 \end{bmatrix} = \begin{bmatrix} -2 \\ 8 \\ 31 \end{bmatrix}$$

Solving (actually not required in this question):

$$-2 + 10(8) = 10i_{1} - i_{2} - 3i_{3} + 10(-i_{1} + 10i_{2} - 4i_{3}) \implies 78 = 99i_{2} - 43i_{3}$$

$$31 - 3(8) = -3i_{1} - 4i_{2} + 14i_{3} - 3(-i_{1} + 10i_{2} - 4i_{3}) \implies 7 = -34i_{2} + 26i_{3}$$

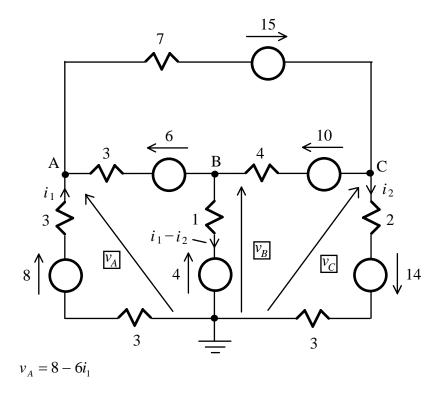
$$26(78) + 43(7) = 26(99i_{2} - 43i_{3}) + 43(-34i_{2} + 26i_{3}) \implies i_{2} = \frac{26(78) + 43(7)}{26(99) - 43(34)} = \frac{2329}{1112}$$

$$34(78) + 99(7) = 34(99i_{2} - 43i_{3}) + 99(-34i_{2} + 26i_{3}) \implies i_{3} = \frac{34(78) + 99(7)}{-34(43) + 99(26)} = \frac{3345}{1112}$$

$$23290 - 4(3345) - 1014$$

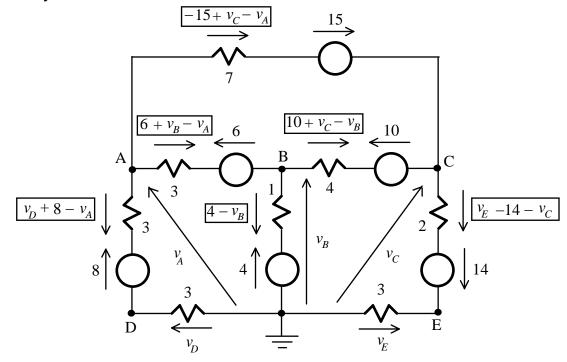
$$i_1 = -8 + 10i_2 - 4i_3 = -8 + \frac{23290}{1112} - \frac{4(3343)}{1112} = \frac{1014}{1112}$$

Voltages of nodes $A\,,\,B\,$ and $C\,$ with respect to ground:



$$v_B = 4 + i_1 - i_2$$
$$v_C = -14 + 5i_2$$

Nodal analysis



Applying KCL to nodes A, B, C, D and E:

$$\frac{v_D + 8 - v_A}{3} + \frac{6 + v_B - v_A}{3} + \frac{v_C - v_A - 15}{7} = 0$$

$$\frac{4 - v_B}{1} + \frac{v_A - v_B - 6}{3} + \frac{v_C - v_B + 10}{4} = 0$$

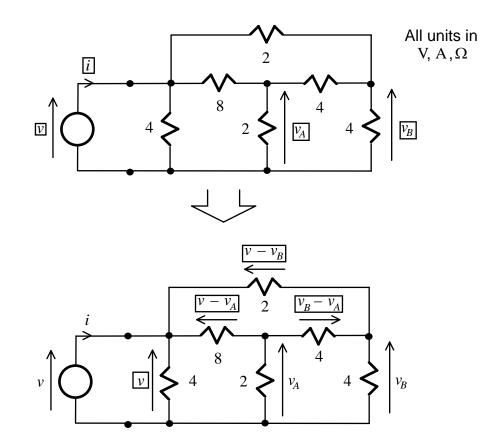
$$\frac{v_E - 14 - v_C}{2} + \frac{v_A - v_C + 15}{7} + \frac{v_B - v_C - 10}{4} = 0$$

$$\frac{v_D + 8 - v_A}{3} + \frac{v_D}{3} = 0$$

$$\frac{v_E - 14 - v_C}{2} + \frac{v_E}{3} = 0$$

In matrix form:

$$\begin{bmatrix} \frac{1}{3} + \frac{1}{3} + \frac{1}{7} & -\frac{1}{3} & -\frac{1}{7} & -\frac{1}{7} & 0 \\ -\frac{1}{3} & 1 + \frac{1}{3} + \frac{1}{4} & -\frac{1}{4} & 0 & 0 \\ -\frac{1}{7} & -\frac{1}{4} & \frac{1}{2} + \frac{1}{7} + \frac{1}{4} & 0 & -\frac{1}{2} \\ \dot{3} & 0 & 0 & -\frac{1}{3} - \frac{1}{3} & 0 \\ 0 & 0 & \frac{1}{2} & 0 & -\frac{1}{2} - \frac{1}{3} \end{bmatrix} \begin{bmatrix} v_A \\ v_B \\ v_C \\ v_D \\ v_E \end{bmatrix} = \begin{bmatrix} \frac{8}{3} + \frac{6}{3} - \frac{15}{7} \\ \frac{1}{4} - 2 + \frac{10}{4} \\ -7 + \frac{15}{7} - \frac{10}{4} \\ \frac{8}{3} \\ -\frac{14}{2} \end{bmatrix}$$



Applying KCL:

$$i = \frac{v}{4} + \frac{v - v_A}{8} + \frac{v - v_B}{2}$$

$$\frac{v_A}{2} + \frac{v_A - v}{8} + \frac{v_A - v_B}{4} = 0 \implies 4v_A + v_A - v + 2v_A - 2v_B = 7v_A - 2v_B - v = 0$$

$$\frac{v_B}{4} + \frac{v_B - v_A}{4} + \frac{v_B - v}{2} = 0 \implies v_B + v_B - v_A + 2v_B - 2v = -v_A + 4v_B - 2v = 0$$

Eliminating v_A and v_B :

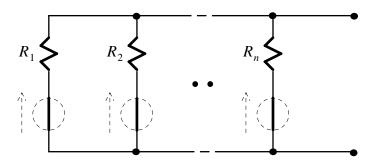
$$(7v_A - 2v_B - v) + 7(-v_A + 4v_B - 2v) = 0 \implies 26v_B - 15v = 0 \implies v_B = \frac{15}{26}v_B$$
$$2(7v_A - 2v_B - v) + (-v_A + 4v_B - 2v) = 0 \implies 13v_A - 4v = 0 \implies v_A = \frac{4}{13}v$$
$$i = \frac{v}{4} + \frac{v - v_A}{8} + \frac{v - v_B}{2} = \left(\frac{1}{4} + \frac{1}{8} + \frac{1}{2}\right)v - \frac{1}{8}\left(\frac{4}{13}\right)v - \frac{1}{2}\left(\frac{15}{26}\right)v = \frac{57}{104}v$$

The equivalent resistance without the 13Ω resistor is therefore

$$\frac{v}{i} = \frac{104}{57} = 1.82\,\Omega$$

and the equivalent resistance with the 13Ω series resistor = 14.82Ω

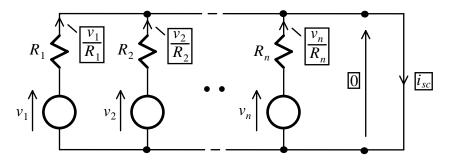
Equivalent resistance



Since the resistors are in parallel, the equivalent resistance R is

$$\frac{1}{R} = \frac{1}{R_1} + \dots + \frac{1}{R_n}$$

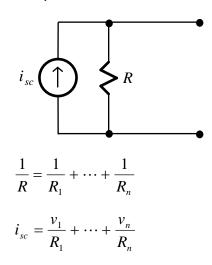
Short circuit current



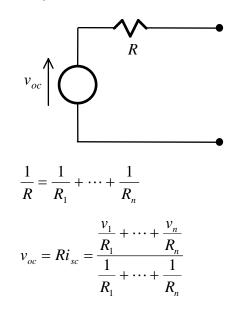
Applying KCL:

$$i_{sc} = \frac{v_1}{R_1} + \dots + \frac{v_n}{R_n}$$

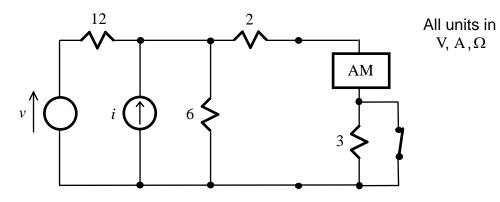
Norton's equivalent circuit



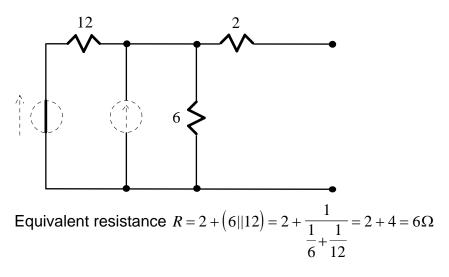
Thevenin's equivalent circuit



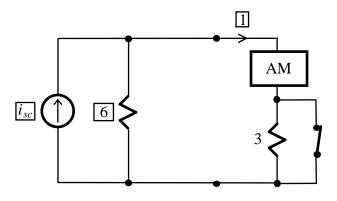
Re-drawing original circuit



Equivalent resistance without 3Ω resistor



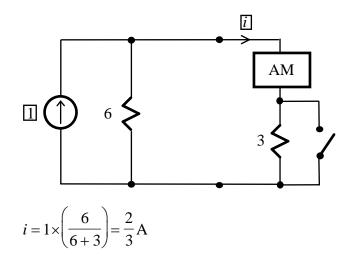
Using Norton's equivalent circuit

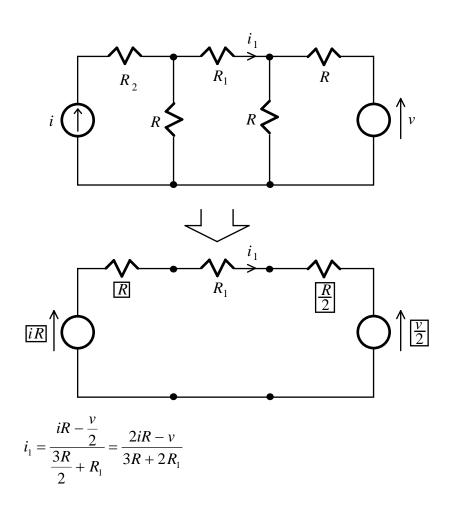


Since AM reads 1A

$$i_{sc} = 1A$$

Thus, when the switch is open

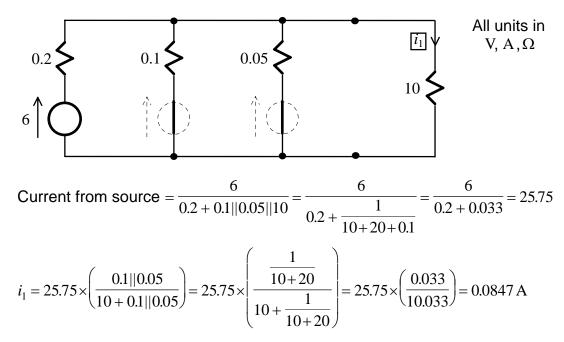




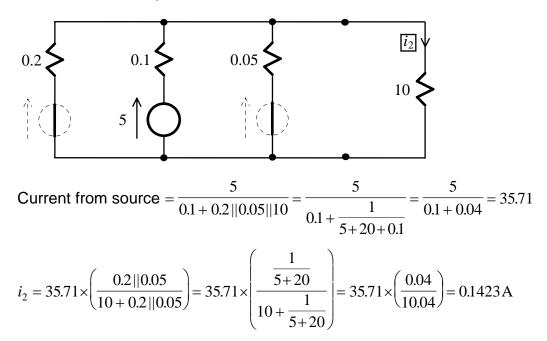
F.4 DC Circuit Analysis II

Q.1

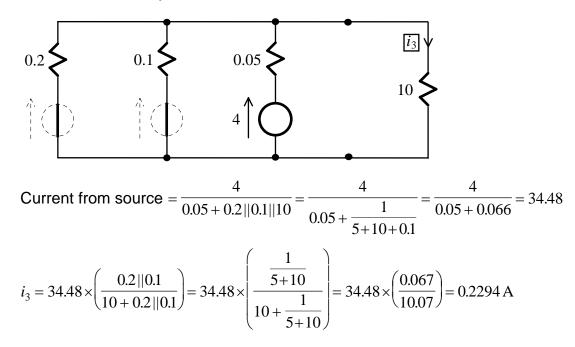
Load current due to Battery 1



Load current due to Battery 2



Load current due to Battery 3

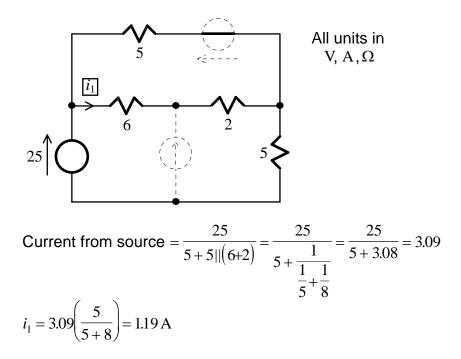


Actual load current

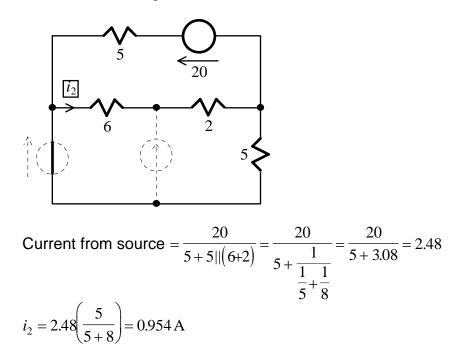
$$i = i_1 + i_2 + i_3 = 0.0847 + 0.1423 + 0.2294 = 0.4564 \,\mathrm{A}$$

Q.2

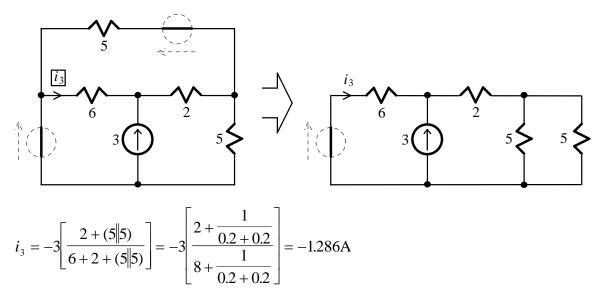
Current due to 25V voltage source



Current due to 20 V voltage source



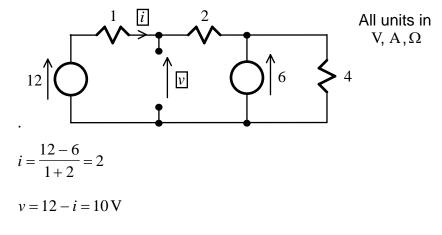
Current due to current source



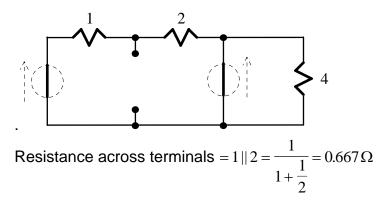
Actual current

 $i = i_1 + i_2 + i_3 = 1.19 + 0.954 - 1.286 = 0.858 \,\mathrm{A}$

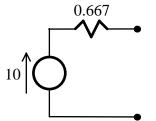
Open circuit voltage



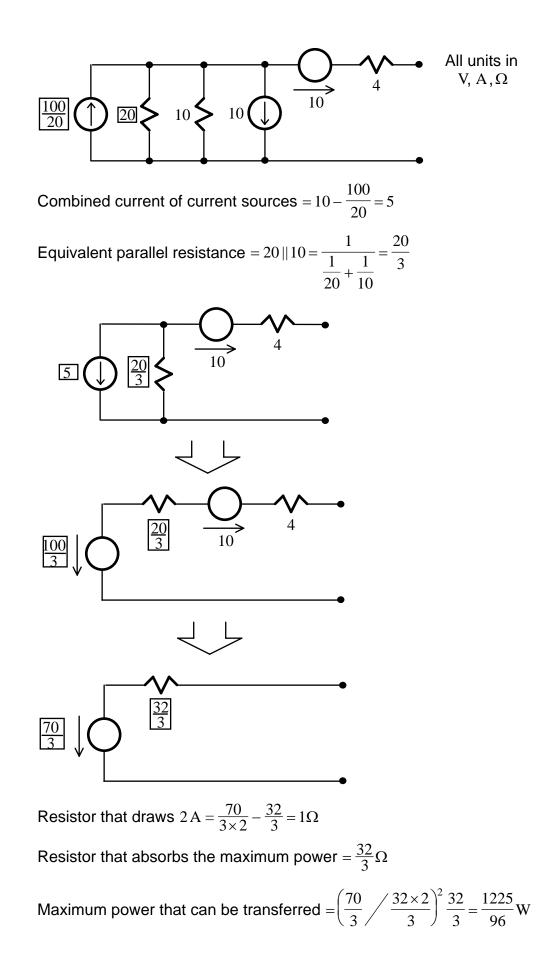
Equivalent resistance



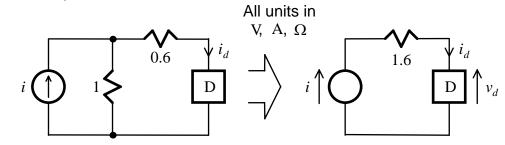
Thevenin's equivalent circuit and maximum power



Maximum power transferable (with a 0.667 Ω load) = $\left(\frac{10}{2 \times 0.667}\right)^2 0.667 = 37.5 \text{ W}$



Thevenin's equivalent circuit

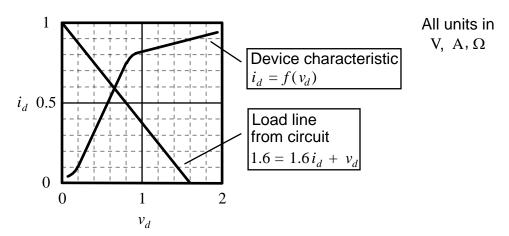


Device current given i = 1.6 A

Applying KVL:

 $i = 1.6i_d + v_d = 1.6$

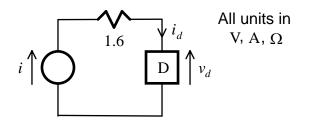
 v_d and i_d can be found from solving this (which gives rise to the load line) and the relationship $i_d = f(v_d)$ given by the characteristic curve. Specifically, when $i_d = 0$, $v_d = 1.6$. Also, when $v_d = 0$, $i_d = 1$.



The point of intersection gives

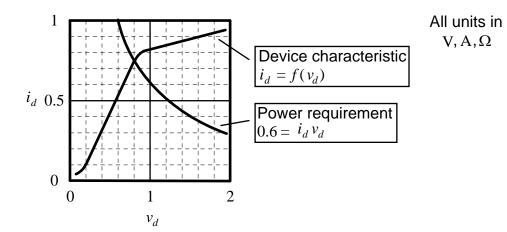
$$i_d = 0.6 \, \text{A}$$

Source current for power dissipated in D to be 0.6W



Power dissipated in $D = i_d v_d = 0.6$

The device voltage and current can be found from solving this and the relationship $i_d = f(v_d)$ given by the characteristic curve:



The point of intersection gives

$$v_d \approx 0.8 \, \mathrm{V}$$

$$i_d \approx 0.75 \,\mathrm{A}$$

From KVL:

$$i = 1.6i_d + v_d \approx 1.6(0.75) + 0.8 = 2$$
 A

Q.6

Voltage gain

Applying KCL to the second half of the circuit:

 $v_2 = -20(50i_1) = -1000i_1$

Applying KVL to the first half of the circuit:

$$v_1 = 2i_1 + \frac{v_2}{5000}$$

Eliminating i_1 :

$$v_1 = -2\left(\frac{v_2}{1000}\right) + \frac{v_2}{5000} = \frac{-1.8v_2}{1000} \implies v_2 = -\frac{1000}{1.8}v_1$$

The voltage gain is

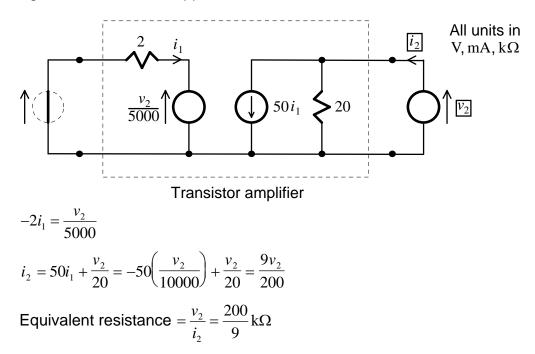
$$\frac{v_2}{v_1} = -\frac{1000}{1.8}$$

The gain in voltage magnitudes is

$$\left|\frac{v_2}{v_1}\right| = \frac{1000}{1.8} = 20\log\left(\frac{1000}{1.8}\right) dB = 55 dB$$

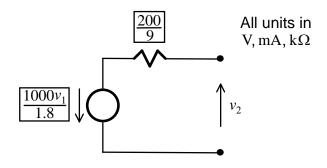
Equivalent resistance

To determine the equivalent resistance as seen from the output terminals, all independent sources have to be replaced by their internal resistances and a voltage source has to be applied to these two terminals:



Note that in calculating this resistance or in using superposition, dependent sources must not be replaced by their internal resistances.

Thevenin's equivalent circuit



F.5 AC Circuit Analysis I

Q.1

	(a)	(b)
AC waveform	$5\sqrt{2}\sin(\omega t)$	$5\sqrt{2}\cos(\omega t)$
	$=5\sqrt{2}\cos\left(\omega t-\frac{\pi}{2}\right)$	$= \operatorname{Re}\left[\left(5e^{j0}\right)\left(\sqrt{2}e^{j\omega t}\right)\right]$
	$= \operatorname{Re}\left[\left(5e^{-j\pi/2}\right)\left(\sqrt{2}e^{j\omega t}\right)\right]$	
Peak value	$5\sqrt{2}$	$5\sqrt{2}$
Frequency	$\omega \operatorname{rad/s} = \frac{\omega}{2\pi} \operatorname{Hz}$	$\omega \operatorname{rad/s} = \frac{\omega}{2\pi} \operatorname{Hz}$
RMS value	5	5
Phase	$-\frac{\pi}{2} = -90^{\circ}$	0
Phasor	5 $e^{-j\pi/2} = 5 / -90^{\circ}$	$5 e^{j0} = 5 / 0^0 = 5$

	(c)	(d)
AC waveform	$10\sqrt{2}\sin(20t+30^0)$	$120\sqrt{2}\cos(314t-45^{\circ})$
	$= 10\sqrt{2}\cos(20t - 60^{0})$	$= \operatorname{Re}\left[\left(120e^{-j\pi/4}\right)\left(\sqrt{2}e^{j314t}\right)\right]$
	$= \operatorname{Re}\left[\left(10e^{-j\pi/3}\right)\left(\sqrt{2}e^{j20t}\right)\right]$	
Peak value	$10\sqrt{2}$ $120\sqrt{2}$	
Frequency	$20 \operatorname{rad/s} = 3.18 \operatorname{Hz}$	314 rad/s = 50 Hz
RMS value	10	120
Phase	$-\frac{\pi}{3} = -60^{\circ}$	$-\frac{\pi}{4} = -45^{\circ}$
Phasor	$10e^{-j\pi/3} = 10/-60^{\circ}$	$120e^{-j\pi/4} = 120/-45^{\circ}$

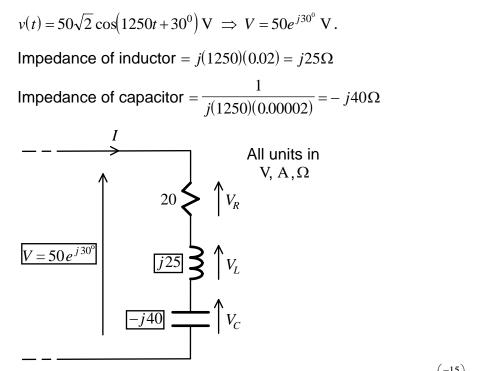
	(e)	(f)
AC waveform	$-50\sin\left(4t - \frac{\pi}{3}\right)$ $= 35.4\sqrt{2}\cos\left(4t - \frac{\pi}{3} - \frac{\pi}{2} + \pi\right)$	$0.25 \cos(2t + 100^{0})$ = 0.177\sqrt{2}\cos(2t + 1.75) = \text{Re}[(0.177e^{j1.75})(\sqrt{2}e^{j2t})]
	$= \operatorname{Re}\left[\left(35.4e^{j\pi/6}\right)\left(\sqrt{2}e^{j4t}\right)\right]$	
Peak value	50	0.25
Frequency	4 rad/s = 0.637 Hz	2 rad/s = 0.318 Hz
RMS value	35.4	0.177
Phase	$\frac{\pi}{6} = 30^{\circ}$	$1.75 = 100^{\circ}$
Phasor	$35.4e^{j\pi/6} = 35.4/30^{\circ}$	$0.177e^{j1.75} = 0.177 \underline{/100^{\circ}}$

(a)	$\frac{100}{\sqrt{2}}e^{j30^{\circ}}\mathrm{V}$	$\operatorname{Re}\left[\left(\frac{100}{\sqrt{2}}e^{j\pi/6}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right] = 100\cos\left(314t + \frac{\pi}{6}\right)V$
(b)	$115e^{j\pi/3}\mathrm{V}$	$\operatorname{Re}\left[\left(115e^{j\pi/3}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right] = 115\sqrt{2}\cos\left(314t + \frac{\pi}{3}\right)V$
(c)	$-0.12e^{-j\pi/4}$ A	$\operatorname{Re}\left[\left(-0.12e^{-j\pi/4}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right] = \operatorname{Re}\left[\left(e^{j\pi} 0.12e^{-j\pi/4}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right]$
		$= 0.12\sqrt{2}\cos\left(314t + \frac{3\pi}{4}\right)A$
(d)	-0.69 <u>/60°</u> A	$\operatorname{Re}\left[\left(-0.69e^{j\pi/3}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right] = \operatorname{Re}\left[\left(e^{j\pi} 0.69e^{j\pi/3}\right)\left(\sqrt{2}e^{j100\pi t}\right)\right]$
		$= 0.69\sqrt{2}\cos\left(314t + \frac{4\pi}{3}\right)$
		$= 0.69\sqrt{2}\cos\left(314t - \frac{2\pi}{3}\right)A$

From
$$v_R(t) = 12\sqrt{2}\cos(2t)$$
 V
Frequency $= \omega = 2 \operatorname{rad/s}$
Impedance of capacitor $= \frac{1}{j\omega 0.1} = \frac{1}{j0.2} = -5j\Omega$
Impedance of inductor $= j\omega 2 = 4j\Omega$
 $V_R = 12e^{j0} = 12$
 $\downarrow V_L \uparrow I_R$
 $V_R \uparrow 3$
 $I_R = \frac{V_R}{3} = 4 \Rightarrow i_R(t) = 4\sqrt{2}\cos(2t)$ A
 $V_L = (4j)I_R = 16j$ V $\Rightarrow v_L(t) = 16\sqrt{2}\cos(2t + \frac{\pi}{2})$ V
 $V_C = V_R + V_L = 12 + 16j$
 $I = I_R + \frac{V_C}{-5j} = 4 - \frac{12 + 16j}{5j} = 4 + 2.4j - 3.2 = 0.8 + 2.4j$
Imag
 2.4
 $I = I_R + \frac{V_C}{-5j} = 4 - \frac{12 + 16j}{5j} = 4 + 2.4j - 3.2 = 0.8 + 2.4j$
 $I = I_R + \frac{V_C}{-5j} = 4 - \frac{12 + 16j}{5j} = 4 + 2.4j - 3.2 = 0.8 + 2.4j$

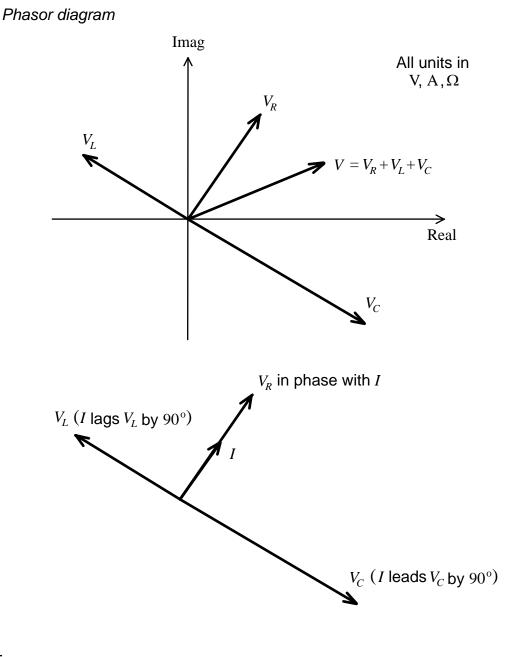
Arg[I] = Arg
$$[0.8 + 2.4 j]$$
 = $\tan^{-1}\left(\frac{2.4}{0.8}\right)$ = 1.25
 $I = 0.8 + 2.4 j = 2.53e^{j1.25} \implies 2.53\sqrt{2}\cos(2t + 1.25)$ A

Impedances and phasors



Total impedance = $20 + j25 - j40 = 20 - j15 = \sqrt{20^2 + 15^2}e^{j\tan^{-1}\left(\frac{-15}{20}\right)} = 25e^{-36.9^{\circ}}\Omega$

$$I = \frac{V}{20 + j25 - j40} = \frac{50e^{j30^{\circ}}}{25e^{j-36.9^{\circ}}} = 2e^{j66.9^{\circ}} \text{ A}$$
$$V_R = 20I = (20)(2e^{j66.9^{\circ}}) = 40e^{j66.9^{\circ}} \text{ V}$$
$$V_L = j25I = (25e^{j90^{\circ}})(2e^{j66.9^{\circ}}) = 50e^{j156.9^{\circ}} \text{ V}$$
$$V_C = -j40I = (40e^{-j90^{\circ}})(2e^{j66.9^{\circ}}) = 80e^{-j23.1^{\circ}} \text{ V}$$



Components

The impedances of series RL, RC and LC circuits are

$$\begin{split} &Z_{RL} = R + j\omega L = R + j100\pi L \\ &Z_{RC} = R + \frac{1}{j\omega C} = R - \frac{j}{100\pi C} \\ &Z_{LC} = j\omega L + \frac{1}{j\omega C} = j \bigg(100\pi L - \frac{1}{100\pi C} \bigg) \end{split}$$

20 + j30 must correspond to a series *RL* circuit with components:

$$R_1 + j100\pi L_1 = 20 + j30 \implies R_1 = 20\Omega \text{ and } L_1 = \frac{30}{100\pi} = 0.0955 \text{ H}$$

10 - j15 must correspond to a series *RC* circuit with components:

$$R_2 - \frac{j}{100\pi C_2} = 10 - j15 \implies R_2 = 10\Omega \text{ and } C_2 = \frac{1}{100\pi(15)} = 212.3 \,\mu\text{F}$$

Circuit admittance

Circuit impedance =
$$Z = (20 + j30) ||(10 - j15) = \frac{1}{\frac{1}{20 + j30} + \frac{1}{10 - j15}}$$

Circuit admittance = $\frac{1}{Z} = \frac{1}{20 + j30} + \frac{1}{10 - j15} = \frac{20 - j30}{20^2 + 30^2} + \frac{10 + j15}{10^2 + 15^2}$

 $= 0.0154 - j0.0231 + 0.0308 + j0.0462 = (0.0462 + j0.0231)\Omega^{-1}$

Power factor

Power factor =
$$\begin{cases} \text{value} = \cos[\operatorname{Arg}(I) - \operatorname{Arg}(V)] \\ \text{leading / lagging} = \begin{cases} \text{leading, Arg}(I) - \operatorname{Arg}(V) > 0 \\ \text{lagging, Arg}(I) - \operatorname{Arg}(V) < 0 \end{cases}$$

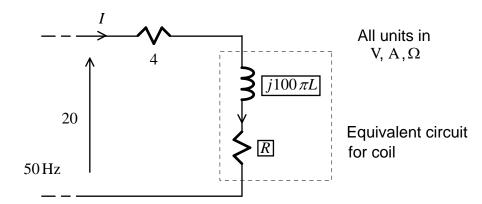
Arg(I) - Arg(V) = Arg $\left(\frac{I}{V}\right)$ = Arg $\left(\frac{1}{Z}\right)$ = - Arg(Z)
= Arg(0.0462 + j0.0231) = tan⁻¹ $\left(\frac{0.0231}{0.0462}\right)$ = 0.464
Power factor
$$\begin{cases} \text{value} = \cos(0.464) = 0.894 \\ \text{leading / lagging} = \begin{cases} \text{leading, } 0.464 > 0 \\ \text{lagging, } 0.464 < 0 \end{cases}$$

= $\begin{pmatrix} 0.894 \\ \text{leading / lagging} = \begin{cases} \text{leading, } 0.464 > 0 \\ \text{lagging, } 0.464 < 0 \end{cases}$
= $\begin{pmatrix} 0.0231 \\ 0.0231 \\ 0.0462 \\ 0.$

F.6 AC Circuit Analysis II

Q.1

Circuit diagram



Main equations

 $\begin{aligned} |\text{Voltage across } 4\Omega| &= |4I| = 9 \implies |I| = \frac{9}{4} \\ |\text{Voltage across coil}| &= \left| I \left(R + j100\pi L \right) \right| = 14 \\ |R + j314L| &= \frac{14}{|I|} = 14 \left(\frac{4}{9} \right) = 6.222 \implies R^2 + (314L)^2 = 38.72 \\ |\text{Supply}| &= \left| I \left(4 + R + j100\pi L \right) \right| = 20 \\ |4 + R + j314L| &= \frac{20}{|I|} = 20 \left(\frac{4}{9} \right) = 8.889 \implies (R + 4)^2 + (314L)^2 = 79.01 \end{aligned}$

Component values

$$\left[\left(R+4 \right)^2 + (314L)^2 \right] - \left[R^2 + (314L)^2 \right] = 79.01 - 38.72$$

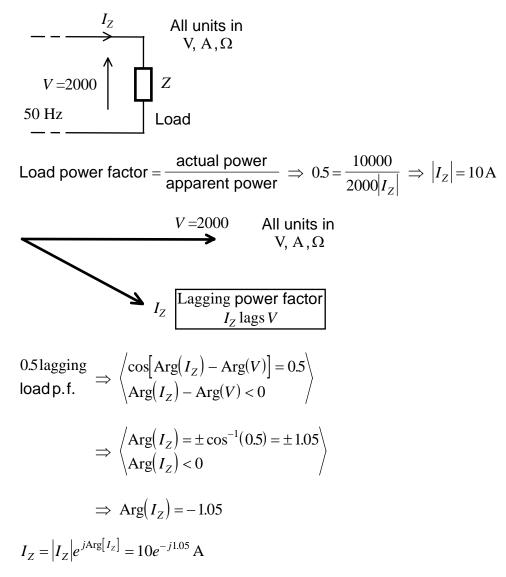
$$8R + 16 = 40.29 \implies R = \frac{40.29 - 16}{8} = 3.04\Omega$$

$$R^2 + (314L)^2 = 38.72 \implies (314L)^2 = 38.72 - 3.04^2 = 29.48 \implies L = \frac{\sqrt{29.48}}{314} = 0.0173 \,\mathrm{H}$$

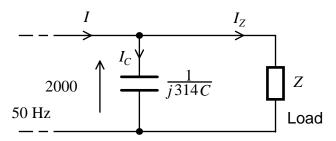
Power and power factor

Power absorbed by coil = $|I|^2 R = \left(\frac{9}{4}\right)^2 (3.04) = 15.4 \text{ W}$ $Power \ factor = \begin{pmatrix} value = cos[Arg(current) - Arg(voltage)] \\ leading / lagging = \begin{cases} leading, Arg(current) - Arg(voltage) > 0 \\ lagging, Arg(current) - Arg(voltage) < 0 \end{cases}$ $Arg(current) - Arg(voltage) = Arg\left(\frac{current}{voltage}\right)$ $= Arg \left(\frac{1}{Impedance}\right) = -Arg (Impedance)$ Power factor = $\begin{pmatrix} value = cos[Arg(impedance)] \\ leading / lagging = \begin{cases} leading, Arg(impedance) < 0 \\ lagging, Arg(impedance) > 0 \end{cases}$ $Arg(impedance) = Arg(R+4+j314L) = Arg(3.04+4+j314\times0.0173)$ $= \operatorname{Arg}(7.04 + j5.43) = \tan^{-1}\left(\frac{5.43}{7.04}\right) = 0.657$ Power factor = $\begin{pmatrix} \text{value} = \cos(0.657) = 0.792 \\ \text{leading / lagging} = \begin{cases} \text{leading, } 0.657 < 0 \\ \text{lagging, } 0.657 > 0 \end{pmatrix} = \begin{pmatrix} 0.792 \\ \text{lagging} \end{pmatrix}$

Load current

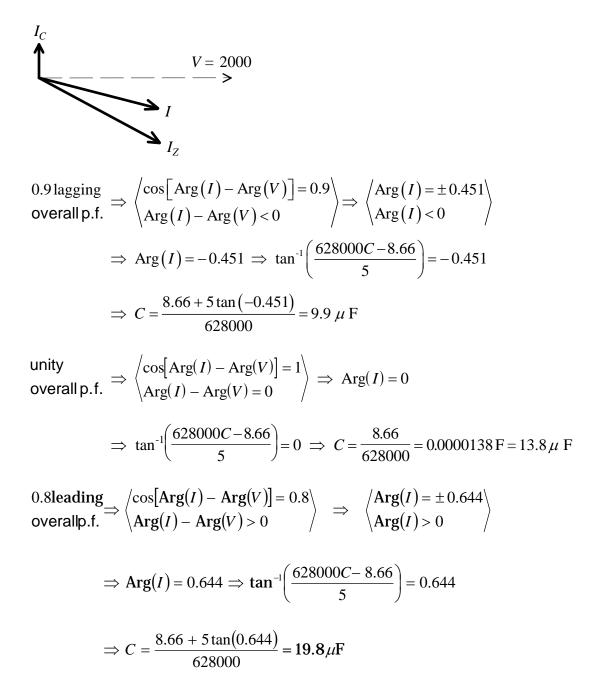


Power factor improvement

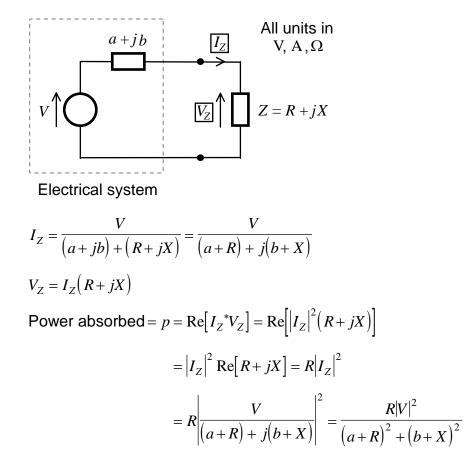


$$I_C = (j314C)(2000) = j628000C$$

$$I = I_Z + I_C = 10e^{-j1.05} + j628000C = 5 + j(62800C - 8.66)$$



Power



Maximum power transfer

For maximum p, the denominator should be as small as possible. As the numerator does not depend on X and the smallest value for (b+X) is 0, maximum power will be absorbed if

$$X = -b$$

so that

$$p = \frac{\left|V\right|^2 R}{\left(a+R\right)^2}$$

Differentiating:

$$\frac{dp}{dR} = |V|^2 \left[\frac{1}{\left(a+R\right)^2} - \frac{2R}{\left(a+R\right)^3}\right] = |V|^2 \left[\frac{a-R}{\left(a+R\right)^3}\right]$$

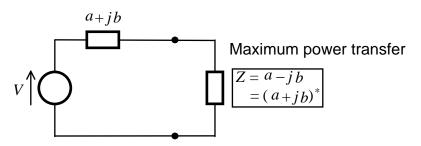
Thus, maximum p occurs when

$$R = a$$

and the maximum power transferable is

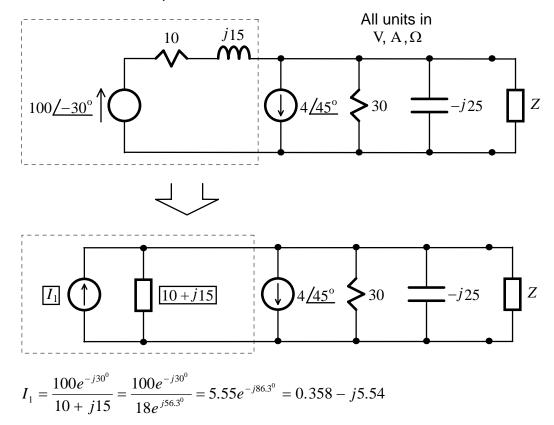
$$p = \frac{|V|^2 R}{(R+R)^2} = \frac{|V|^2}{4R} = \frac{|V|^2}{4a} W$$

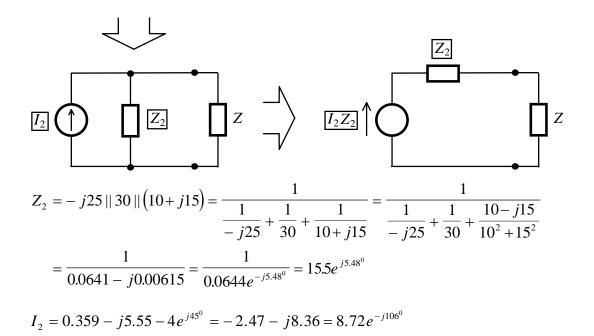
In general, maximum power transfer occurs when the load impedance is equal to the conjugate of the Thevenin's or Norton's impedance. When this occurs, the total impedance is purely resistive and the current and voltage in the circuit are in phase:



Q.4

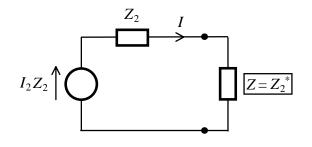
Norton's and Thevenin's equivalent circuit





Maximum power transfer

From the previous problem, this occurs when



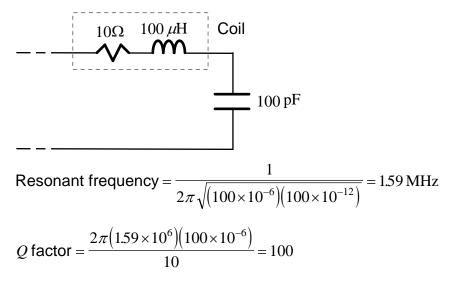
$$Z = Z_2^{*} = \left(15.5e^{j5.48^{\circ}}\right)^{*} = 15.5e^{-j5.48^{\circ}}$$

Total impedance = $Z + Z_2 = Z_2^* + Z_2 = 15.5e^{j5.48^0} + 15.5e^{-j5.48^0} = 2[15.5\cos(5.48^0)]$

$$I = \frac{I_2 Z_2}{\left(Z + Z_2\right)} = \frac{8.72 e^{-j106^0} 15.5 e^{j5.48^0}}{2\left[15.5 \cos(5.48^0)\right]} = \frac{8.72 e^{-j101^0}}{2\cos(5.48^0)}$$

Thus, the maximum power transferable is

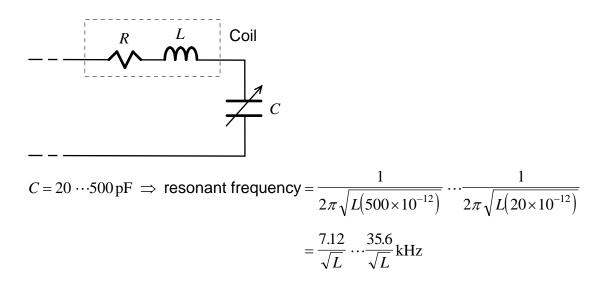
$$\operatorname{Re}\left[I^{*}(IZ)\right] = |I|^{2} \operatorname{Re}\left[Z_{2}^{*}\right]$$
$$= \left|\frac{8.72e^{-j101^{0}}}{2\cos(5.48^{0})}\right|^{2} \operatorname{Re}\left[15.5e^{-j5.48^{0}}\right] = \left|\frac{8.72}{2\cos(5.48^{0})}\right|^{2} 15.5\cos(5.48^{0})$$
$$= \frac{8.72^{2} \times 15.5}{4\cos(5.48^{0})} = 297 \operatorname{W}$$



Since the *Q* factor is large, the circuit is bandpass in nature with

3dB cutoff frequencies
$$\approx 1.59 \left(1 \pm \frac{1}{200}\right)$$
MHz
Bandwidth $\approx \frac{1.59 \times 10^6}{100} = 15.9$ kHz

Q.6



For the lowest tunable frequency to be 666 kHz:

$$666 = \frac{7.12}{\sqrt{L}} \implies L = \left(\frac{7.12}{666}\right)^2 = 0.114 \text{ mH}$$

The highest tunable frequency is then $\frac{35.6}{\sqrt{L}} = \frac{35.6}{\sqrt{0.114 \times 10^{-3}}} = 3.42 \text{ MHz}$